

CARMAN								
<b>Team Members:</b>	Ethan, John, Troy, Neal, Zack, Jeremy, Ben, Jonathan		<b>Team Leader:</b>	Ethan Miller, Zack Martin				
Task #	Task	Owner	Start Date	End Date	Percent			
<b>4</b>	<b>CarMan</b>				77.52%	80.62%	76.42%	
4.1	Inventor Models	Ben			74%			
4.1.1	Enclosure Sides	Jonathan			95%			
4.1.2	Enclosure Bottom	Jonathan			90%			
4.1.3	Enclosure Top	Jonathan			95%			
4.1.4	Relay Board	Ben			90%			
4.1.5	Motor Controller	Ben			50%			
4.1.6	Interior Components	Jonathan			10%			
4.1.7	Enclosure Back	Jonathan			90%			
4.2	Wiring Diagrams	all		10/20/2020	100%			
4.2.1	New Board Connections	Troy,John		10/20/20	100%			
4.2.2	Carman Interconnect	Ethan, Zack		10/20/20	100%			
4.3	Board Layouts			11/3/20	100%			
4.3.1	GLV Board (KiCAD)	Zack, Troy	10/20/20	10/27/20	100%			
4.3.2	TSI Board (KiCAD)	John, Nate, Ethan	10/20/20	10/27/20	100%			
4.3.3	Relay Board (Visio)	Ben, Zack			100%			
4.3.4	Logic Board (KiCAD)	Zack, Troy, Ethan	10/20/20	10/27/20	100%			
4.3.5	GLV Board (PCB Layout)	Zack, Jeremy	10/27/20	11/3/20	100%			
4.3.6	TSI Board (PCB Layout)	John, Nate	10/27/20	11/3/20	100%			
4.3.7	Logic Board (PCB Layout)	Ethan, Zack	10/27/20	11/3/20	100%			
4.4	Parts Ordered			11/6/20	95%			
4.4.1	GLV Parts				100%			
4.4.2	TSI Parts				100%			
4.4.3	Logic				100%			
4.4.4	Relay Board				80%			
4.5	Parts Sent to Manufacturing				100%			
4.5.1	Enclosure	Jonathan			100%			
4.6	Parts Received				100%			
4.6.1	Enclosure	Jonathan			100%			
4.7	Parts Assembled				97%			
4.7.1	GLV Board				100%			
4.7.2	TSI Board				100%			
4.7.3	Relay Device				95%			
4.7.4	Logic Board				100%			
4.7.5	Enclosure	Jonathan			90%			
4.8	Firmware	Ethan			50%			
4.9	CarMan Assembled				85%			

4.10	Test Plan Created				42%			
4.10.1	TSI	John, Nate			100%			
4.10.2	Logic	Troy, Zack, Ethan			85%			
4.10.3	GLV	Troy, Zack, Jeremy			100%			
4.10.4	Board Interconnection	all			0%			
4.10.5	Relay Board	Ben			0%			
4.10.6	Firmware	Ethan			10%			
4.10.7	Carman Integration	all			0%			
4.11	Test Plan Conducted				49%			
4.11.1	TSI				100%			
4.11.2	Logic				95%			
4.11.3	GLV				100%			
4.11.4	Board Interconnection				40%			
4.11.5	Relay Board				0%			
4.11.6	Firmware				0%			
4.11.7	Carman Integration				10%			
4.12	BOM				75%			
4.12.1	TSI			10/30/20	100%			
4.12.2	Logic			10/30/20	100%			
4.12.3	GLV			10/30/20	100%			
4.12.4	Enclosure				0%			
4.13	Website Documentation Updated				80%			
4.13.1	TSI	John, Nate			95%			
4.13.2	Logic	Troy, Zack, Ethan			95%			
4.13.3	GLV	Troy, Zack, Jeremy			95%			
4.13.4	Relay Board	Ben, Zack			75%			
4.13.5	Safety Loop Diagram	Troy, Ethan			98%			
4.13.6	Enclosure	Jonathan			0%			
4.13.7	Carman	all			85%			
4.13.8	Re-partitioned Board Functionality Spreadsheet	all			100%			