



Pin	Color	Signal	Pin	Color	Signal	Pin	Color	Signal	Pin	Color	Signal	Pin	Color	Signal	Pin	Color	Signal						
1	GRN	24VDC +	1	GRN	24VDC +	1	ORG	TSV HV	1	ORG	TSV HV	1	ORG	TSV HV	1	ORG	TSV HV	1	ORG	TSV HV			
2	BLK	LV RTN	2	BLK	LV RTN	2	BLK	LV RTN	2	BLK	LV RTN	2	BLK	LV RTN	2	BLK	LV RTN	2	BLK	LV RTN	2	BLK	LV RTN
3	WHT	TSAL Left	3	WHT	TSAL Left	3	WHT	TSAL Left	3	WHT	TSAL Left	3	WHT	TSAL Left	3	WHT	TSAL Left	3	WHT	TSAL Left	3	WHT	TSAL Left
4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN	4	BLK	TSAL Left RTN
5	WHT	SPARE	5	WHT	SPARE	5	WHT	SPARE	5	WHT	SPARE	5	WHT	SPARE	5	WHT	SPARE	5	WHT	SPARE	5	WHT	SPARE
6	BLK	SPARE	6	BLK	SPARE	6	BLK	SPARE	6	BLK	SPARE	6	BLK	SPARE	6	BLK	SPARE	6	BLK	SPARE	6	BLK	SPARE
7	WHT	RTDS	7	WHT	RTDS	7	WHT	RTDS	7	WHT	RTDS	7	WHT	RTDS	7	WHT	RTDS	7	WHT	RTDS	7	WHT	RTDS
8	BLK	RTDS RTN	8	BLK	RTDS RTN	8	BLK	RTDS RTN	8	BLK	RTDS RTN	8	BLK	RTDS RTN	8	BLK	RTDS RTN	8	BLK	RTDS RTN	8	BLK	RTDS RTN

EDITED BY
 DRAWN BY **Drew Carleton**
 APPROVED BY ECE
 APPROVED BY ME
 DATE **2019 - 04 - 08**

LAFAYETTE Lafayette College
 Easton, Pa
 ECE Department

TITLE **LFEV CAR/DYNO ELECTRICAL SYSTEM DIAGRAM**

SIZE F	DWG NO TOP LEVEL	ASSIGNMENT SENIOR DESIGN I1	REV 1.1.0
COURSE TOP LEVEL	SHEET 1 OF 1		

Connect to TSI where packs are during Dyno room testing instead of the packs.