

TSV Delivered (15%)	TSV 1	Fix "Active Light" on Control PCB (5%)	TSV 1.1	Verify and document correct working of active light (25%)						
			TSV 1.2	Circuit/Code corrected (25%)						
			TSV 1.3	Error Found in code/circuit (25%)						
			TSV 1.4	Verified existence of problem in light (25%)						
	TSV 2	AMS Delivered (20%)	TSV 2.1	Testing new AMS boards (40%)	TSV 2.1.1	Record AMS boards status (25%)				
					TSV 2.1.2	28 Functional AMS boards (25%)				
					TSV 2.1.3	verify/calibrate using the AMSVU (25%)				
					TSV 2.1.4	Document boards and their current status (25%)				
			TSV 2.2	AMS boards watchdog frequency recognized (40%)	TSV 2.2.1	Fixed AMS boards watchdog frequency (50%)				
					TSV 2.2.2	Record list of parameters (watchdog frequency) of AMS boards which are not recognized (50%)				
			TSV 2.3	Connect Monitoring Connector to one AMS board (20%)					Schematic showing wiring and location of monitoring connector	
	TSV 3	PACKMAN Delivered (25%)	TSV 3.1	Replace Crystal on PacMAN with better part (25%)						
			TSV 3.2	4 PACMan working with test unit (25%)	TSV 3.2.1	4 PACMan with potential Workability (35%)				
					TSV 3.2.2	Ensure all components are on the board (35%)				
					TSV 3.2.3	Ensure CAN bus working on all PackMan (30%)				
			TSV 3.3	Functional USB UART (25%)	TSV 3.3.1	Establish UART communication with TSV				
			TSV 3.4	Limit lower boundary of cell voltage (25%)	TSV 3.4.1	Test circuit with fixed lower limit in code (35%)				
					TSV 3.4.2	Fix code for lower limit (35%)				
			TSV 3.4.3	Determine source of problem for limitation (30%)						
	TSV 4	Individual PACKS Delivered (15%)	TSV 4.1	Update Fuse in Packs (200A to 300A) (35%)						
			TSV 4.2	Clocks working properly in all Packs (35%)	TSV 4.2.1	verify correct working of packs 3 and 4 (25%)				
					TSV 4.2.2	Fixed Pack 3 clock (25%)				
					TSV 4.2.3	Fixed Pack 4 clock (25%)				
					TSV 4.2.4	Record current status of packs 3 and 4 clock (25%)				
	TSV 4.3	Pack 1 Rewired (30%)	TSV 4.3.1	Replaced Wiring of Pack 1 (50%)						
			TSV 4.3.2	List of Wires that need to be changed (50%)						
	TSV 5	Mechanical Parts Delivered (10%)	TSV 5.1	Engrave part numbers on all mechanical parts (50%)	TSV 5.1.1	Engrave individual parts (50%)				
					TSV 5.1.2	List of parts to be engraved (50%)				
			TSV 5.2	Firewall Connected (50%)						
	TSV 6	SOC Algorithm Programming (15%)	TSV 6.1	Discharging Algorithm (50%)	TSV 6.1.1	Test plan (25%) Shuyu Jia 02/02/2018				
					TSV 6.1.2	Circuit Modeling (25%) Shuyu Jia 02/09/2018				
					TSV 6.1.3	Data collection(25%) Shuyu Jia 02/16/2018				
TSV 6.1.4					Data analysis (25%) Shuyu Jia 02/23/2018					
TSV 6.2			Charging Algorithm (50%)	TSV 6.2.1	Test plan (25%) Shuyu Jia 03/02/2018					
				TSV 6.2.2	Circuit Modeling (25%) Shuyu Jia 03/09/2018					
				TSV 6.2.3	Data collection (25%) Shuyu Jia 03/16/2018					
				TSV 6.2.4	Data analysis (25%) Shuyu Jia 03/23/2018					
TSV 7	Management (10%)	TSV 7.1	PDR Delivered (10%)							
		TSV 7.2	CDR Delivered (20%)							
		TSV 7.3	User Manual Delivered (20%)							
		TSV 7.4	Maintenance Manual Delivered (20%)							
		TSV 7.5	Final Poster Delivered (10%)							
		TSV 7.6	Parts List/Bill of Materials (20%)							