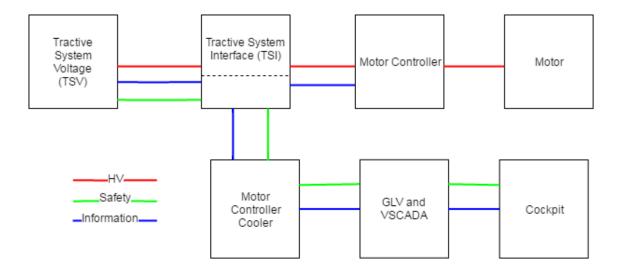


LFEV - High Level Simplified Block Diagram





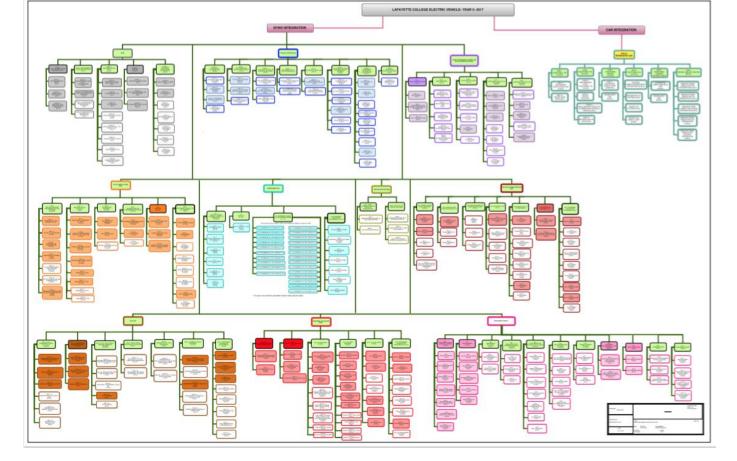


Work Breakdown Structure

• WBS

• PSL

• Schedule





ENGINEERING

Interconnect / Cabling



- Complete Interconnection of all car subsystems
- Identifying and acquiring/purchasing all required parts for complete integration
- Assembling each cable based on schematic and subsystem specifications
- Cable Testing
- Cable Labeling
- Cable Reworking based on System Diagram Updates
- Acceptance Test Plan Wire Documentation



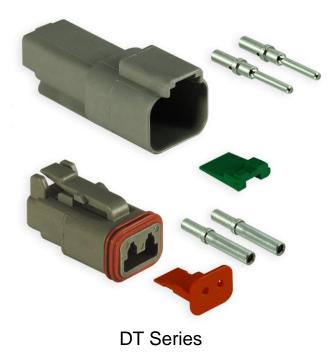


Interconnect / Cabling

- Wires gauge based on voltage and current
- Connectors
 - Deutsch DT Series
 - o ITT Cannon PowerLock



PowerLock



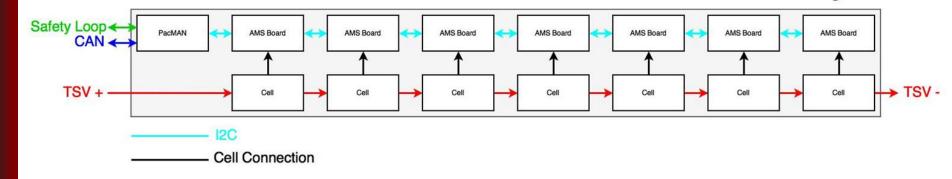


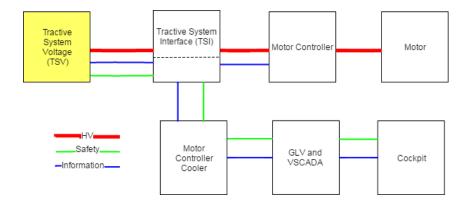




Pack Block Diagram

Tractive System Voltage [TSV]

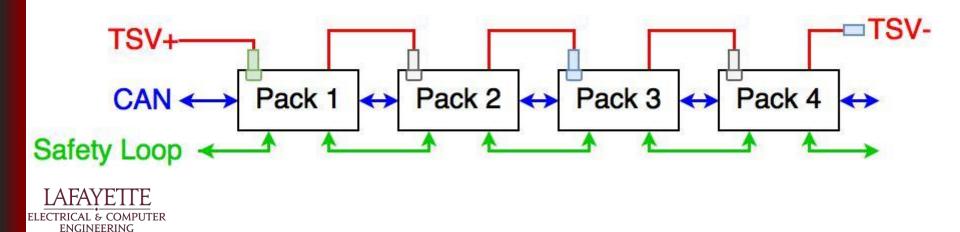


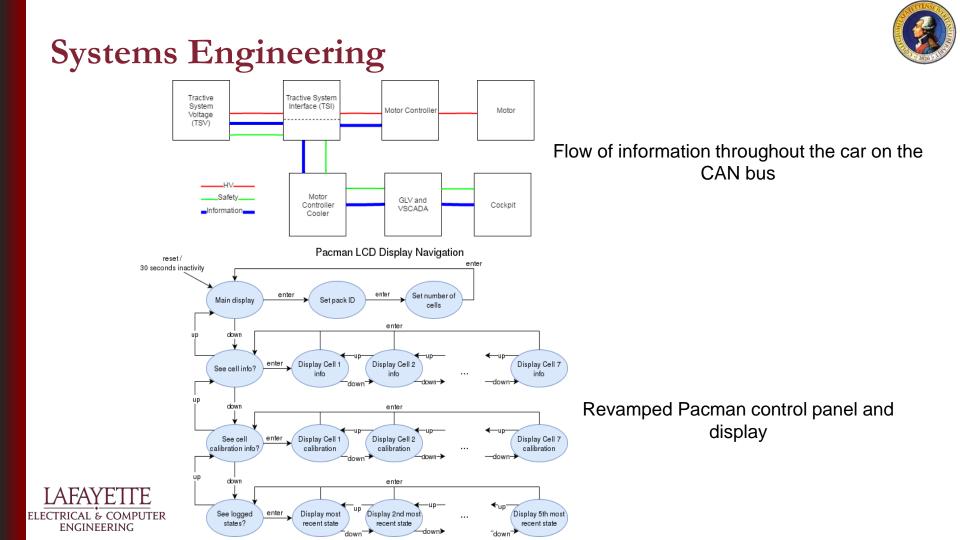




Tractive System Voltage [TSV]

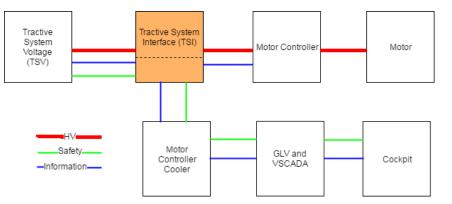
- Produced 3 Additional TSV Accumulator Packs
 - Maintained the existing TSV Accumulator Pack
 - Recommended slight modifications to PacMAN
- Tested integration of accumulator, dynamometer, GLV, and basic SCADA (up to 160A)
- Comprehensive Documentation of TSV Subsystem





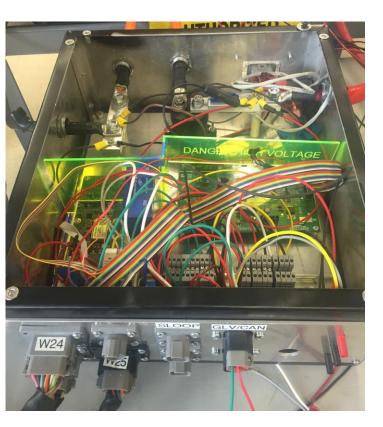
Tractive System Interface [TSI]

- PCB Schematic Design
- PCB Layout
- Box Integration and Wiring





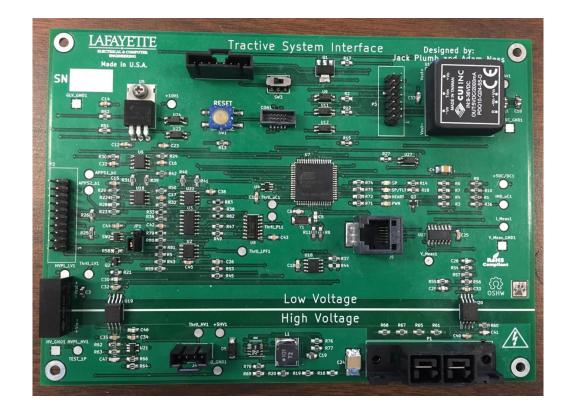






Tractive System Interface [TSI]

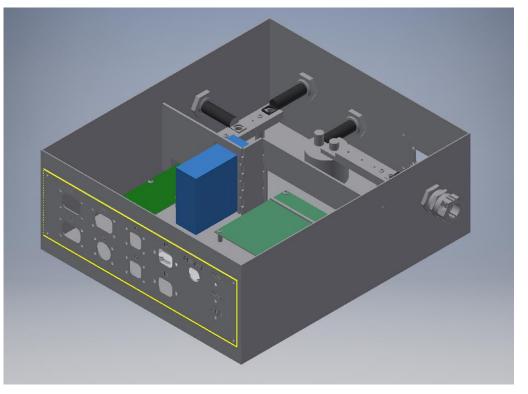
- PCB Schematic Design
 O Throttle Plausibility
- PCB Layout
- PCB Build and Debug
- Firmware





Tractive System Interface [TSI]

- Inventor Box Layout
- IMD Integration
- HV Bars

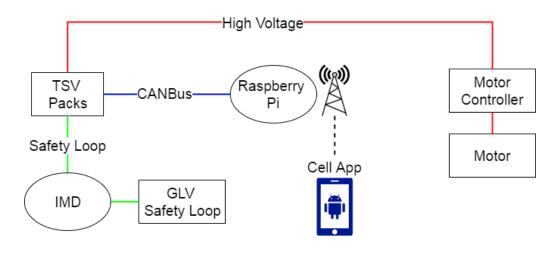






Dynamometer

- Ran tests required for other teams
- Fixed communication to the power supply to be able to control power supply and obtain current Amperage and Voltage
- Expanded upon the existing code base to add new functionality and added new calculation parameters
- Assisted other teams in integrating their subsystems into the Dyno system.



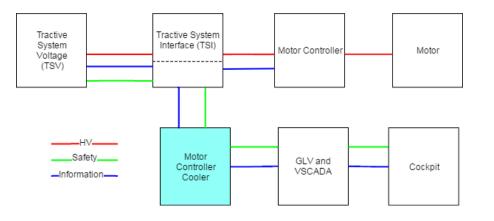




Controller Cooling

Software Design of Cooling Controller

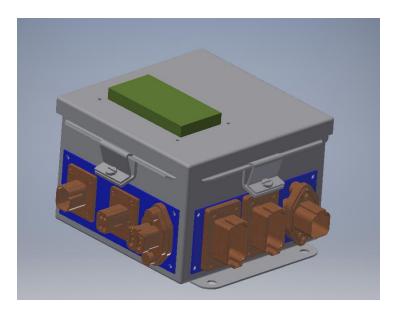
- Sensors integration
- Mode selection
 - Auto change temp threshold in run-time
 - Manual change speed % in run-time
- LCD interface
- CAN communication
- 24V Safety-Loop relay control





Controller Cooling

- Design and Build the box of cooling controller
 - Mounting of arduino board, circuit board, connector panels, etc.
- Preliminary documentation
 - Drawing of Mounting
 - Block diagram
 - Electrical schematic diagram

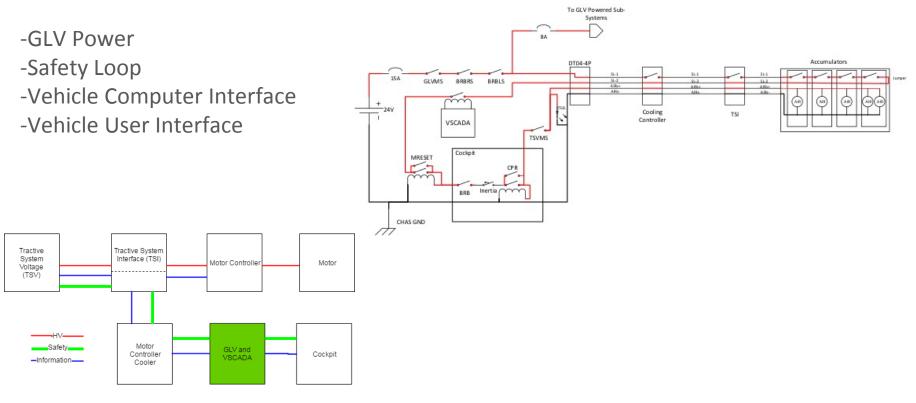








Grounded Low Voltage [GLV]





Grounded Low Voltage [GLV]



- Battery research and acquisition
- GLV_BOB schematic, layout, parts acquisition and testing
- Car Integration

ELECTRICAL & COMPUTER ENGINEERING

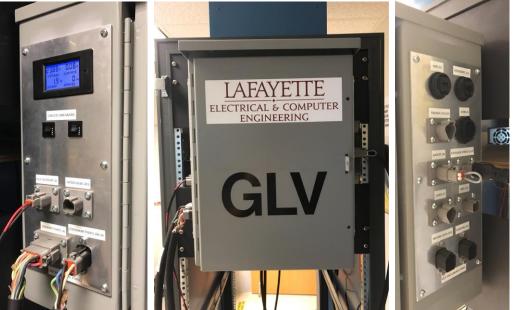


ADC/DAC/Temp Sensor using Vref = 2.5V (internal) +5V -12 TO BUFF MReset A MReset_A OUT.3 A 2MV2T GND Current & Voltage sensor 2C Address 1000000 DC/DC 24V -> 5V OUT GND Trim Ax IK GND GLV POW 2 GND Optoisolator used to Inform SCADA on safety loop status 1Yo 3 +5V SCADA Relay Crash protection relay Inverting PFET (on when gate is GND) GND High Side Load Switch MResetA CPR A Pos side of Fault LED 2V drop across LED 512 S CPR.B external master reset relay 100V Dyno room supply ecoupling Caps MReset_B AIRS+ MReset A 1 TESTCIRLA 1 3 MReset_B 3 TESTCIRL B 12C Pell up resistors Engineer: Chris Bennett & Kyle Phillips CAN-+51 Supervisor: Chris Nadovich 10 Connectors Raunherry Pi ribbon cable ECE 492 Spring 2017 OUT Lafayette College 2 104 Sheet: / File: GLV_BoB.sch 106 thr. Title: GLV/VSCADA/Safety BoB SDA Size: A4 Date: 2017-KiCad E.D.A. kicad 4.0 2-stab Rev: 0.2

Grounded Low Voltage [GLV]

- Mechanical components of the GLV system
 - Enclosure Layout
 - Enclosure panels
 - Dyno room panels
- Dyno room integration









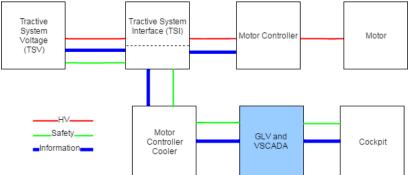


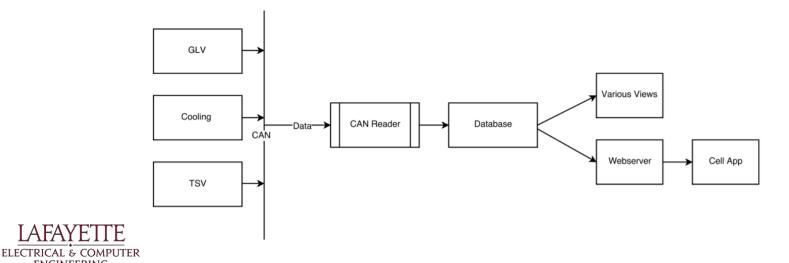
Vehicle Supervisory Control and Data Acquisition [VSCADA] Tractive System Tractive

- Backend database
 - Can dump -> database
- User Interface

ENGINEERING

• Graphics

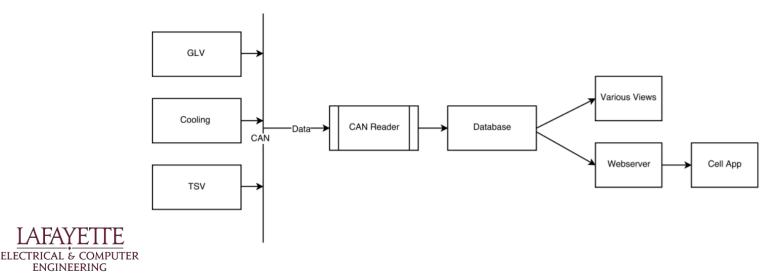






Vehicle Supervisory Control and Data Acquisition [VSCADA]

- Webserver
 - Handles GET requests
 - Can query based on parameters
- User Interface
 - More graphics



(REG 6 LINE)

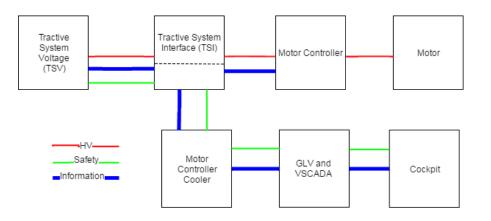
John Gehrig Board [JGB]

• UART

- Fixed and modified original library
- Implemented two way communication

• CAN Bus

- Implemented receiving
- Keyboard to CAN / CAN Dump using UART



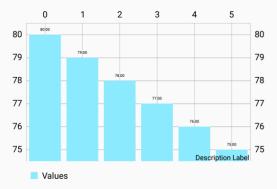


Cell App

- Android Application (Version 4.0.3+)
 - Connection to VSCADA through web server
 - O Display of data with various views
 - Automatically updating data and views
 Fully customizable interface





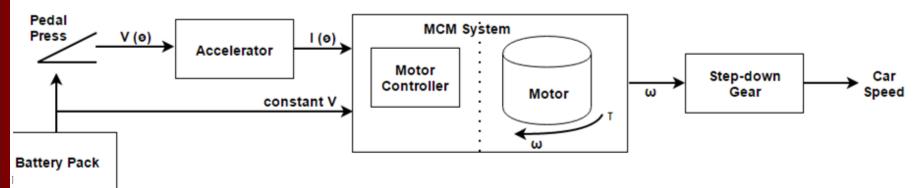


Line0.0 Line1.0 Line2.0 Line3.0 Line4.0 Line4.0



Physics Modeling & Cruise Control

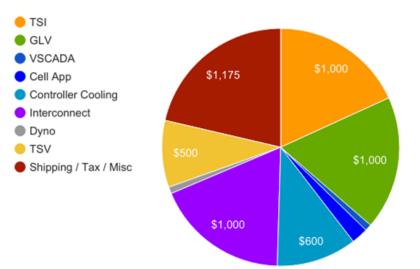
- Research of physical relation of integration of Formula Electric car
- Research of theoretical physical behaviour of MCM system
- Experimental determination of:
 - Mathematical relation of MCM I/O
 - Electric motor plausibility for Formula Electric car
 - Dynamic and Static Model lookup table
 - MCM system efficiency
 - Accurate foundation for cruise control



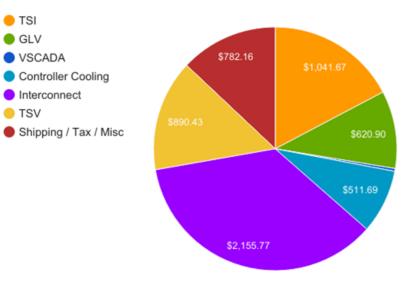
Budget



Allocated Budget Breakdown



Overall Spending Breakdown





Communications



- Worked to raise awareness of the work being done on campus
- Documented throughout the semester the work being done and produced overall video summary documenting the "essence" of the project
- Research Proposal for what the next big ECE 492 project should be
- User Manual Videos

