

Potential Areas for Improvement

Areas of Concern, Not immediate Problems

Problem in PCB, NEEDS to be fixed

Footprint isn't quite the right size, it will need to be made smaller to be soldered onto the board well

Increase spacing between traces Here

VERSION 4.3

Add ~5V Zener Diode & Ceramic Cap

If you want to be an over-achiever, the copper area and trace length should be minimized.

Probably not worthwhile...

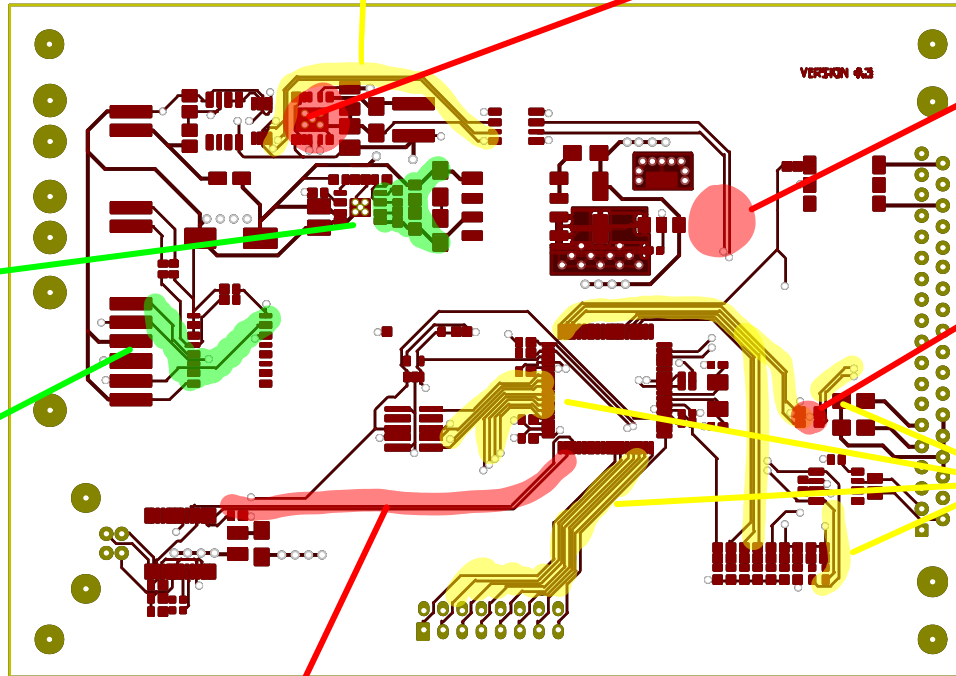
I don't like the routing here...

Is there a better way?

Footprint too big, find another or customize and make smaller  
Should be powered from 5V, not 3.3V

Increasing trace spacing here wouldn't be a bad idea

May or may not be backwards... Investigate...



Engineers: John Gehrig  
Supervisor: Christopher Nedavich  
Fall Semester 2015  
Lafayette College

Annotations By John Gehrig

Sheet:  
File: pacman-main.kicad\_pcb

Title: Battery Pack Management Computer

Size: USLetter Date: 2015-10-08

Rev: 0.3

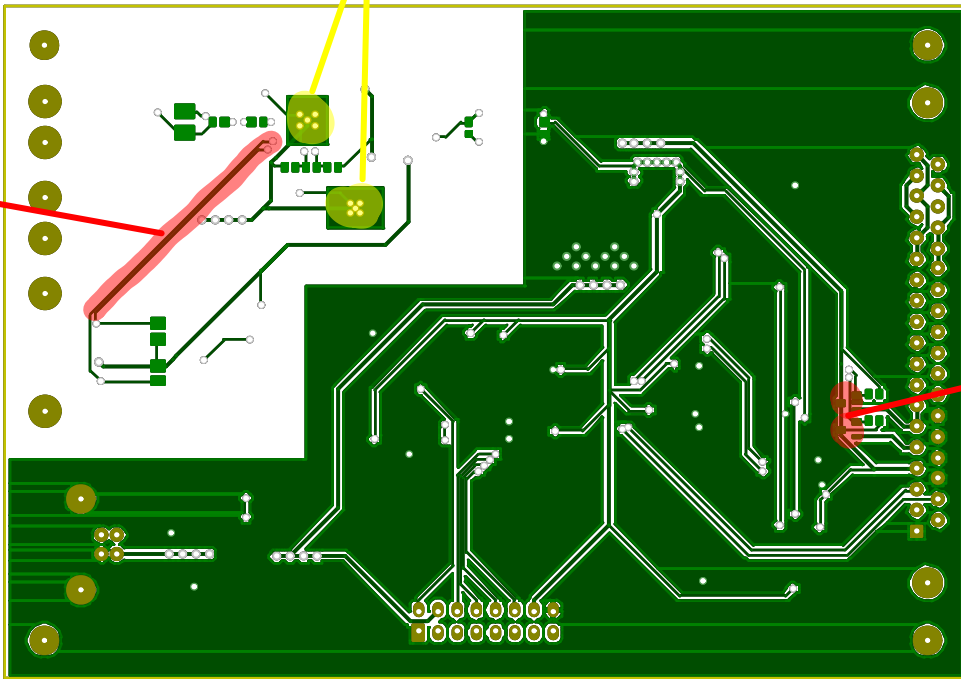
KiCad E.D.A. kicad (after 2015-may-25 BZR unknown)-product

Id: 1/1

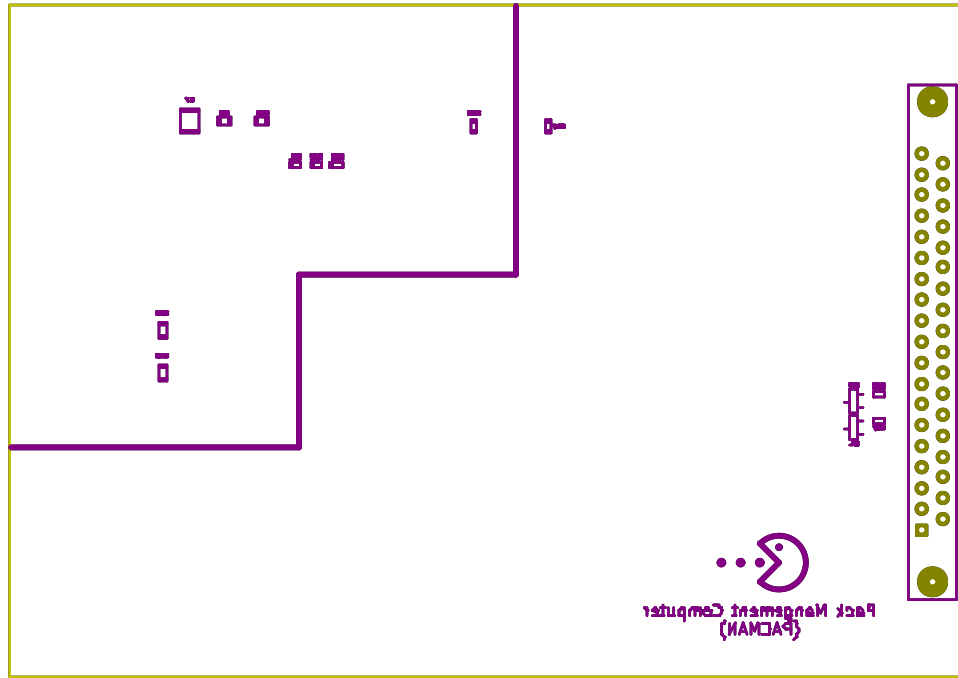
Bigger through holes, or one bit through hole might make this iron solderable... currently hot air is necessary

Trace spacing must be increased

PFET Footprint has wrong pin-number order



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 Pack Management Computer (PACMAN)

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 Lafayette College

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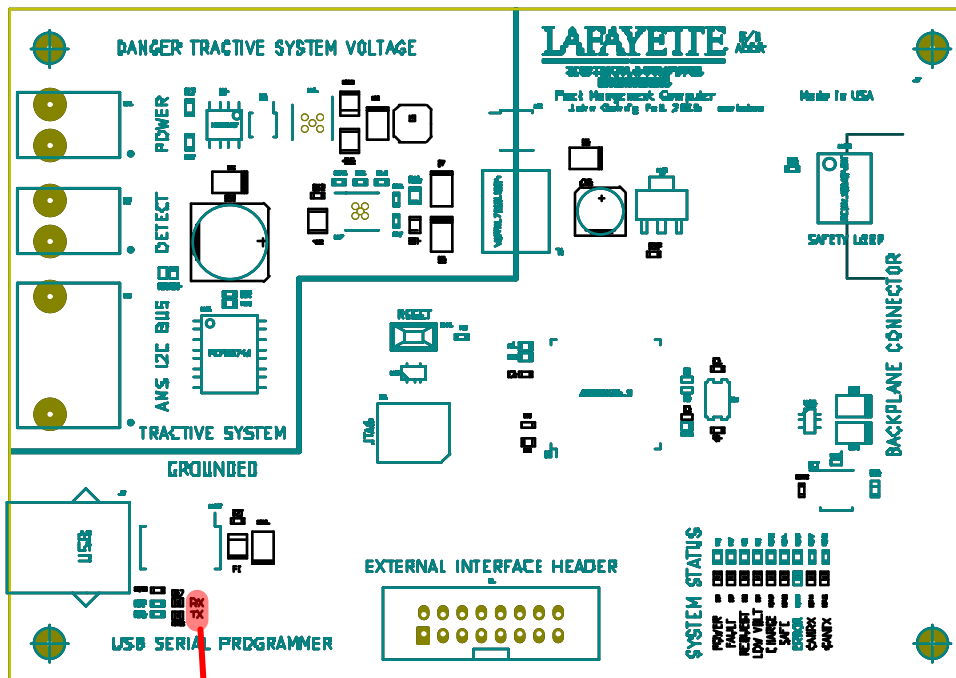
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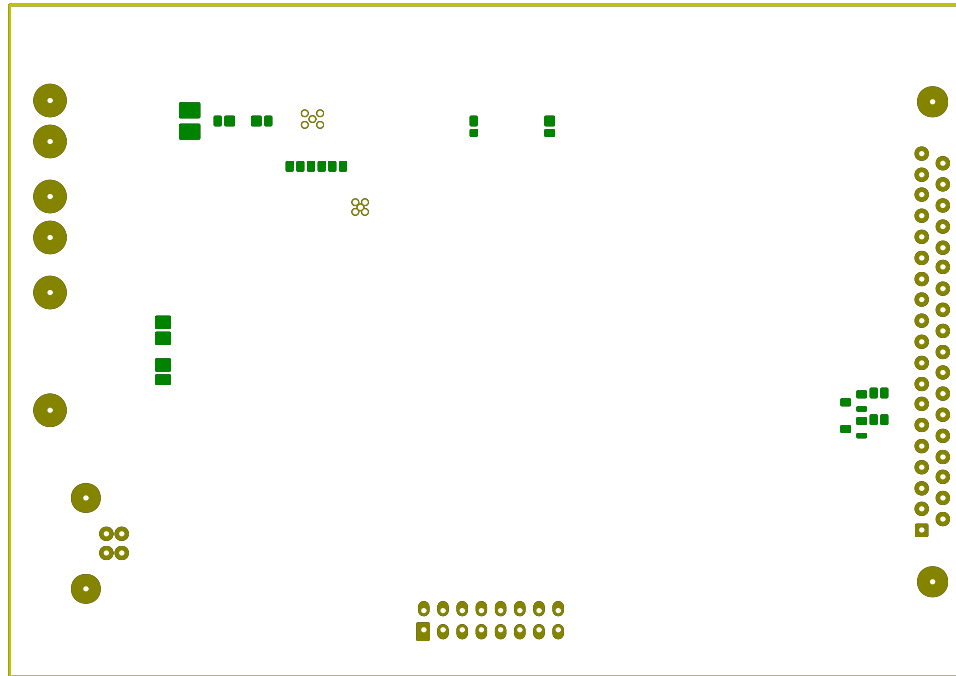


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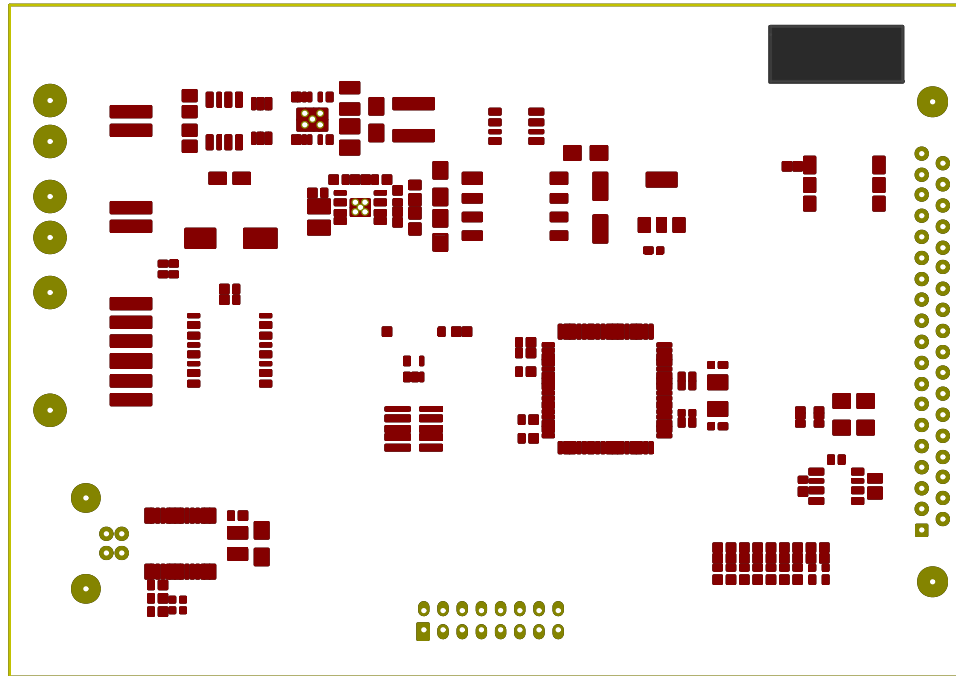
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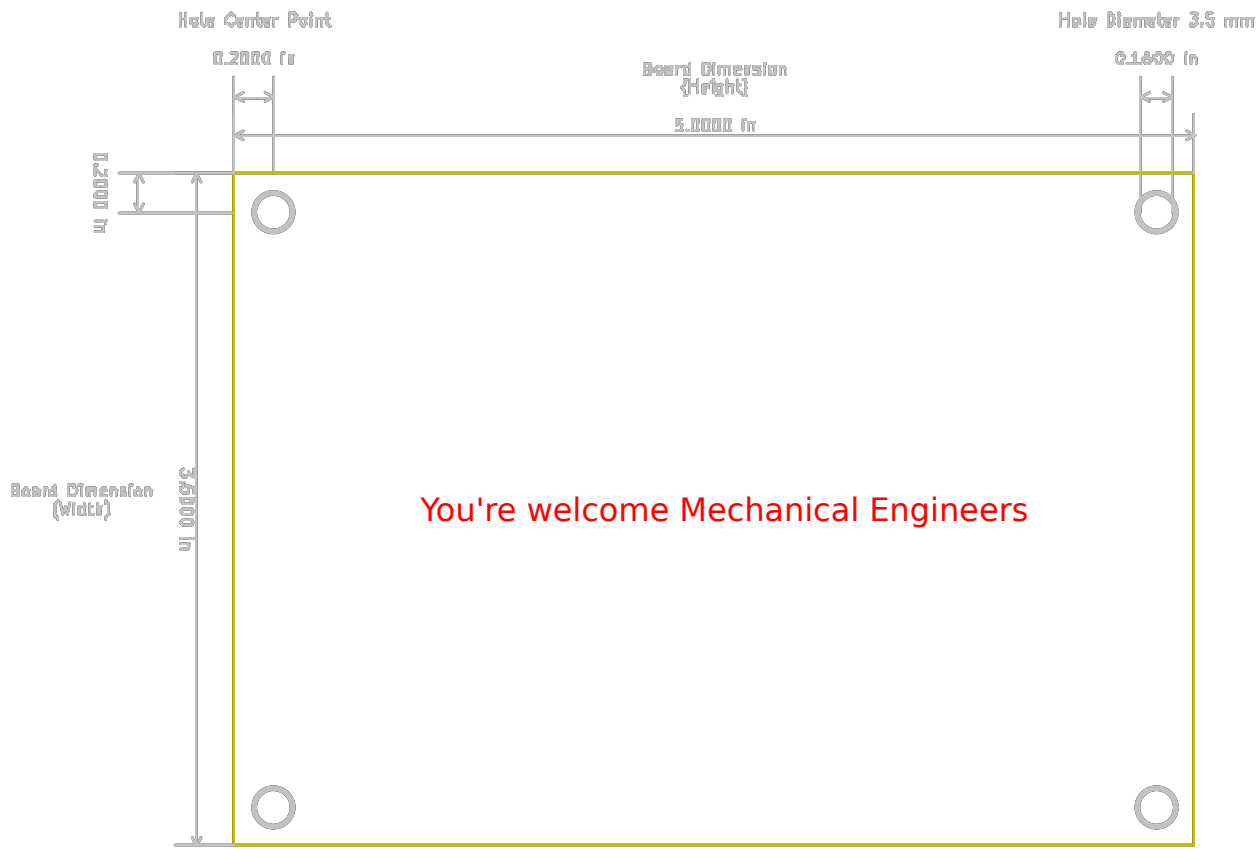
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You're welcome Mechanical Engineers

REVISION 0.3

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