



PacMan BoB V 2.0
ECE 492 LFEV-2015
LOGO LAFAYETTE
ELECTRICAL & COMPUTER
ENGINEERING
G***



Board Design By: William Stathis '15

Sheet:	
File: BoB.kicad_pcb	
Title:	
Size: A4	Date:
KiCad E.D.A. pcbnew (after 2015-mar-04 BZR unknown)-product	Rev:
	Id: 1/1